

PCN 25

Raspberry Pi CM5 IO Board

Revision 2

Raspberry Pi Ltd

Colophon

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Version 1.0

Build date: 19/06/2025

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Document version history

Release	Date	Description
1.0	1 Mar 2025	Initial release
1.1	19 Jun 2025	Correction to notification and transition dates

Product Change Notification

Notification ID

25

Notification date

19th June 2025

Title

Change of Raspberry Pi CM5 IO board to revision 2

Products Affected

CM5 IO board

Reason for Change

Improved fan control, minor BOM improvements

Change Description

Old PCB revision: 1

Old software model number: N/A

New PCB revision: 2

New software model number: N/A

Raspberry Pi Ltd have extensively tested and qualified alternatives which closely match the original parts in terms of specification and performance.

Mechanical (Form, Fit, Function) Changes

No change

Electrical

Changes to the PCB to improve fan control in shutdown states.

Adds a pull-up resistor to the FAN output to support fans that don't have an internal pull-up. This also ensure the fan is turned off during power down as **CM5_3 . 3v** will be low.

Simplify the LED driving circuit to remove a superfluous buffer.

Silkscreen

Fixes silkscreen errors for the USB_OTG_ID pin and Sync_Out pin.

Software/Firmware Changes Required

None

Transition Date(s)

June 2025

There will be a transition period where both old and new units are available in the distribution channel.

Identification Method to Distinguish Change

The new product can be distinguished by visual inspection of the top surface of the PCB. The silkscreen now has R2 indicated just below the Compute Module 5 IO board graphic, which is in turn below the Raspberry Pi Logo. See the red box in the image below.

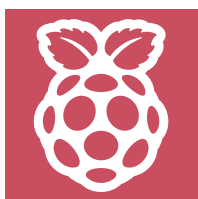
Table 1: New revision identification



Contact Details for more information

Please contact applications@raspberrypi.com if you have any queries about this PCN.

Web: www.raspberrypi.com



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